

Order Code :21224782.1 Name : Digital Lab Trainer





## Specifications:-

• Study and Construction of Various Digital Electronics Lab Experiment

# **Technical Specifications:**

# **Power Supplies:**

- Operated on Mains power 230V, 50Hz +10%
- DC Power Supplies IC Regulated 0-15V, 500mA
- DC Power Supplies IC Regulated 0-30V, 500mA
- DC Power Supplies Fixed +5V, 1A
- DC Power Supplies Fixed -5V,500mA

## **Function Generator:**

• 0-1MHz Pulse Generator

# Components are mounted on the panels are:

- Logic Inputs & Outputs 12 Bit
- Buzzer as Continuity Tester
- Logic Probe Inbuilt.
- 7 Bit 2mm to 4mm Conversion Sockets.
- Low Frequency Clock Generator 1Hz-10Hz.
- Logic Low Frequency Mono Pulsar.
- 2 Bit BCD To 7 Segment Indicators With Inputs A, B, C, D Inputs.
- Breadboards: Unique solder -less large size, spring loaded breadboard consisting of one Terminal Strips with 640 tie points each and 2 Distribution Strips with 100 tie points each, totaling to 840 tie points. (Size: 55mm X 170mm approx).

Note: Specifications are subject to change, Photos shown above are Indicative, Actual Product can Vary.

### TESCA TECHNOLOGIES PVT. LTD.

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#### **Salient Features:**

- Front panel built with high class insulated Printed Circuit Board sheet with well printed circuits and symbols.
- Fuse for Short Circuit protection
- Instruction manual.
- Connections are brought out through 2mm Brass Pins.
- Patch Cords 2mm.
- The trainer is housed in ABS Plastic cabinet.
- Size of the trainer set 12"x10"

## **Optional Accessories:**

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### **List of Moduls:**

- Logic gate module
- Digital ic tester
- 4bit parallel adder subtractor
- Encoder-decoder
- Demorgan's theorem
- Bit multiplexer-demultiplexer
- Universal gate nand nor
- 4 bit serial in parallel out shift register
- Flip flop
- Bcd to 7 segment
- Ttl and cmos interfacing
- Flash type a to d converter

## **Electronics Trainer:**

- Synchronous and asychronous counter
- Parity genrator and checker
- Arithmatic logic unit (alu)
- 8 bit mux and dmux
- Full adder and subtractor using nand gate
- Random access memory (ram)
- D to a converter (dac)
- 4 bit comparator
- A to d converter (adc)
- Cmos characteristics
- Ttl characteristics
- 16 bit mux dmux
- Rtl logic gate
- Logic gate using dtl
- Adc and dac converter (r-2r and flash type)
- 8 bit equality detector
- 16 to 4 encoder
- Boolean expression module
- Master slave flip flop
- Code converters

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