



Experimental Training Board has been designed specifically for the study of Binary Rate Multiplier. This Training Board gives a better understanding of the working of binary rate multiplier for frequency division of input in binary steps.

Practical experience on this board carries great educative value for Science and Engineering Students.

Object:

To Study Binary Rate Multiplier to achieve output of desired frequency in binary steps, by dividing the available clock frequency with a selectable number (which is the Binary Rate) using Binary Rate Multiplier IC 4089.

Features:

The board consists of the following built-in parts :

01. + 10V D.C. at 50mA, IC regulated power supply internally connected.
 02. Binary Rate Multiplier CMOS IC 4089.
 03. Timer IC 555.
 04. LEDs for visual indication of status.
 05. SPDT switches for logic selection.
 06. Adequate no. of Electronic Components.
 07. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on 230V \pm 10% at 50Hz A.C. Mains.
 - * Adequate no. of patch cords stackable from rear both ends 4 mm spring loaded plug length $\frac{1}{2}$ metre.
 - * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections & observation of waveforms.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Other Apparatus Required:

- * Frequency Counter 6 digit
- * Dual Trace Cathode Ray Oscilloscope 20MHz

Note: Specifications are subject to change.

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