Computer Logic Training Board has been designed specifically to study the Principle of NOT/BUFFER Logic Gates and to compare the truth table with experimental results. This training board offers a new method of training students in the basic theory of digital circuits and make them familiar with basic experiments in digital circuits. The board is having integral input arrangements and make the students familiar with the NOT/BUFFER function. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.

Object:

To study the principle of NOT/BUFFER logic gates and to compare the truth table with experimental results.

Features:

The board consists of the following built-in parts :

01.   + 5V D.C. at 100mA, IC Regulated Power Supply internally connected.

02.   NOT gate followed by another NOT gate to give BUFFER output.

03.   Switches for logic selection.

04.   LEDs for visual indication of status.

05.   Adequate no. of other Electronic Components.

06.   Mains ON/OFF switch, Fuse and Jewel light.

*   The unit is operative on 230V ±10% at 50Hz A.C. Mains.

*   Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ metre.

*   Good Quality, reliable terminal/sockets are provided at ap propriate places on panel for connections / observation of waveforms.

*   Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.