Training Board Of Karnaugh Mapping Applications. The karnaugh map is the simplest and commonly used method to simplify the boolean expressions. It can be used up to six variables.

Object:

01. To simplify any given boolean function of four variables using karnaugh mapping.
02. Designing & Implementation of two level circuits.

Features:

The board consists of the following built-in parts :

01. D.C. supply : +5v at 200 mA Ic Regulated power supply.
02. Logic Inputs : Eight independent logic level inputs to select High / Low TTL levels, each with a LED to indicate high / low status and termination.
03. Logic Indicator : Two independent logic level indicators for High / Low status indication of digital outputs.
04. IC's on Panel : 2 Input AND Gate IC-7408
: 3 Input AND Gate IC-7411
: 2 Input NAND Gate IC-7400
: 3 Input NAND Gate IC-7410
: 2 Input OR Gate IC-7432
: 3 Input OR Gate IC-4075
: 2 Input NOR Gate IC-7402
: 3 Input NOR Gate IC-7427
05. Seven segment decoder : One BCD to Seven Segment Decoder/ Driver IC with termination.
06. Adequate no. of other electronic Components.
07. The unit is operative on 230v ± 10% at 50 Hz A.C. mains.
08. Mains ON / OFF Switch and LED indicator are provided.

* Adequate nos. of patch cords stackable 2mm spring loaded plug, length ½ metre
* Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.
305, Taru Chhaya Nagar, Tonk Road, Jaipur-302029, India
Tel: +91-141-2724326, Mob: +91-9413330765
Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in