



Logic Simulator (TTL)/Logic Trainer Board has been designed specifically to make the students familiar with the study of ICs and verification of the truth table of logic gates, flip-flops, Gates.

Practical experience on this trainer/board carries great educative value for Science and Engineering Students

Specifications:

- 01 OUTPUT D. C. VOLTAGE : Fixed 5V
- 02 OUTPUT CURRENT : 200mA
- 03 LOAD REGULATION : $\pm 1\%$ of the highest specified output voltage
(NO LOAD TO FULL LOAD)
- 04 RIPPLE AND NOISE : less than 2mV
- 05 LOGIC INPUTS : 10 switches for High/Low
- 06 OUTPUT INDICATORS : 10mm bright Red LEDs
- 07 INPUT VOLTAGE : 230V $\pm 10\%$ at 50 Hz AC Mains
- * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

305, Taru Chhaya Nagar, Tonk Road, Jaipur-302029, India
Tel: +91-141-2724326, Mob: +91-9413330765
Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in

