**Specifications:-**

- Advanced RISC architecture – On-chip 2-cycle multiplier

- High endurance, non-volatile memory segments

- JTAG (IEEE 1149.1 compliant) interface

- Peripheral features – Real-time counter with separate oscillator

- Power-on reset (POR) and programmable brown-out detection

- Internal Calibrated RC oscillator

- External and internal interrupt sources

- Six sleep modes – Idle, ADC noise reduction, power-save, power-down, standby and extended standby

- 32 Programmable I/O lines

- Applications: Industrial, Computers & Computer Peripherals

*Note: Specifications are subject to change, Photos shown above are Indicative, Actual Product can Vary.*

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