



Specifications

- Indicators
 - 10 TTL/CMOS Logic Level Inputs with Dual Color LED indication for logic low and logic high.
 - 10 LED for output indication.
- Clock Generator
 - Fixed Clock generation of 1Hz, 10Hz, 100Hz, 1KHz, 10KHz, 100KHz & 1MHz.
- Pulse Generator
 - Logic Pulsar provides single pole double throw bounce less pulses of Low to High and High to Low transitions
- Logic Probe
 - Logic Probe facility provided with LED indicators to indicate Logic High, Low & Tristate.
- Bread Board Area
 - Four Distribution Strip of 100 tie points each totaling 400 tie points
 - Two Terminal Strip of 630 tie points totaling 1260 tie points.
- Display
 - Four digit seven-segment LED display.
- Power Supplies
 - Fixed DC Power Supply: +5V, $\pm 12V$.
- Interconnections
 - All interconnections are made using 2mm banana Patch cords.
- All ICS are mounted on IC Sockets.
- Bare board Tested Glass Epoxy SMOBC PCB is used.
- Attractive Wooden enclosures of Light weight Australian Pine Wood.
- Set of 2mm Patch cords for interconnections
- User's Manual with sample experimental programs

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension,
Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India,
Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in