



Experimental Training Board has been designed specifically for the study of Phase Locked Loop (PLL) IC 565. This training board covers most of the important parameters, characteristics and applications on Phase-locked Loop (PLL) IC 565.

Practical experience on this board carries great educative value for Science and Engineering Students.

Object:

To measure important parameters of Phase Locked Loop (PLL) IC 565:

VCO Characteristics :

- 01. To study and measure the free running frequency or centre frequency of VCO.
- 02. To study the VCO sensitivity
- 03. To study the VCO linearity PLL Characteristics :
- 04. To study and measure the capture range and lock range.

PLLApplications:

- Frequency synthesis 01.
- 02. F.M. De-modulation
- 03. A.M. De-modulation

FEATURES

The board consists of following built-in parts:

- \pm 6V D.C. at 100mA, IC regulated Power Supply. 1.
- 2. 3. D.C. Voltmeter, 65mm rectangular dial, with switch selectable of ranges 1V and 10V.
- Phase locked loop IC-565
- 4. Two sets of three inter connected sockets for multi-connections wherever required.
- 5. Adequate no. of other electronic components.
- 6. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on $230V \pm 10\%$ at 50Hz A.C. Mains.
- * Adequate no. of patch cords stackable from rear both ends 2mm spring loaded plug length 1/2 metre.
- * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections & observation of waveforms.
- * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Other Apparatus Required:

- Sine Square Wave Generator
- * Digital frequency counter, 6 digit
- Cathode Ray Oscilloscope 20MHz

Note: Specifications are subject to change.

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