36374 experimental training board has been designed specifically for the study of frequency multiplier using PLL (Phase Locked Loop) chip to multiply the input frequency by a factor 5.

This Training board covers most of the important parameters and applications on phase locked loop (PLL) IC 565.

Practical experience on this board carries great educative value for Science and Engineering students.

Object

1. To study frequency multiplier using PLL chip to multiply the input frequency by a factor of five.

Features

The board consists of the following built in parts

1. +10 V and -10 V D.C. at 100mA, IC regulated power supply.
2. 5 V DC at 50mA, IC regulated power supply.
3. Phase Lock Loop (PLL) IC -565
4. The four bit BCD counter IC 7490.
5. Transistor to drive decade counter.
6. Square Wave Generator
7. Adequate no of other electronic components.
8. Mains ON/OFF switch, Fuse and jewel light.
9. The unit is operative on 230V ±10% at 50HZ A.C. Mains.
10. Good quality reliable terminal/sockets are provided at appropriate places on panel for connections and observation of waveforms.
11. Weight : 1.500 Kg. (Approx.)
12. Dimension : W 340 x H 125 x D 210

List of Accessories

1. Patch cords 4mm length 50cm Red 02
2. Patch cords 4mm length 50cm Black 02

Other Apparatus Required

1. Digital Frequency Counter / Dual trace CRO
2. Sine Square Wave Generator

Note: Specifications are subject to change.

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