Computer Logic Training Board has been designed specifically to study Universal Logic Gates and Applications**.** This Training Board is designed to verify the truth table of various logic functions, to prove De-morgan's theorem, Half adder and Full adder by using universal logic gates**.** The board is absolutely self contained and requires no other apparatus**.**

Practical experience on this board carries great educative value for Science and Engineering Students**.**



**Object:**
01**.**      To study the operation and characteristics of a TTL NAND / NOR gate**.**
02**.**      To prove the De-morgan's Theorem by using NOR & NAND gates**.**
03**.**      To perform various logic functions using NOR and NAND gates i**.**e**.** OR, AND, NOT, NOR, NAND, Exclusive OR (EX-OR),
             Half adder and Full adder**.**

**Features:**
The board consists of the following built-in parts :
01**.**      +5V D**.**C**.** at 100mA, IC Regulated Power Supply internally connected**.**
02**.**      Ten, 2-input NAND gates**.**
03**.**      Sixteen, 2-input NOR gates**.**
04**.**      Switches for logic selection**.**
05**.**      LEDs for visual indication of status**.**
06**.**      Adequate no**.** of other Electronic Components**.**
07**.**      Mains ON/OFF switch, Fuse and Jewel light**.**
*          The unit is operative on 230V ±10% at 50Hz A**.**C**.** Mains**.**
*          Adequate no**.** of patch cords stackable from rear both ends 4mm spring loaded plug length ½ metre**.**
*          Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms**.**
*          Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions
             and Book References**.**

Note: Specifications are subject to change.