



Computer Logic Training Board has been designed specifically to familiarise the operation of a demultiplexer circuits. In digital system the technique of multiplexer and demultiplexers are frequently used to reduce the system cost and to improve the reliability. The unit provides the understanding of two output demultiplexers. This concept can be extended to make the demultiplexers for more complex circuits. The unit is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.

Object

To study the operation of a Digital Demultiplexer circuit

Features

The unit consists of the following built-in parts :

01. +5V D.C. at 50mA, IC Regulated Power Supply internally connected.
 02. Two switches to set the input and control bits.
 03. Two LEDs for visual indication of selected line.
 04. A separate LED provides the indication of logic level of any pin of IC.
 05. Adequate no. of other electronic components.
 06. Mains ON/OFF switch, fuse and Neon Indicator are provided.
- * The unit is operative on 230V \pm 10% at 50Hz A.C. Mains.
 - * Adequate nos. of patch cords stackable from rear both ends 4mm spring loaded plug, length ½ metre
 - * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension,
Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India,
Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in