

Computer Logic Training Board has been designed specifically for the study of Parity circuits. This Training Board gives a better understanding of checking & generation of Parity. It has 8-bit binary input, one output for checking parity and another 9-bit output with generated odd or even parity. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.



Object:

01. Verification of even parity or odd parity by using EX-OR gates (parity checker).
02. To generate odd and even parity (parity generator).

Features:

The board consists of the following built-in parts :

01. +5V D.C. at 200mA, IC regulated power supply internally connected.
 02. Two, Quad 2-Input Exclusive OR gate IC's .
 03. Hex Inverter IC.
 04. LEDs (24 No's) for visual indication of status.
 05. SPDT (9 No's) switches for logic selection.
 06. Adequate no. of other Electronic Components.
 07. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on 230V \pm 10% at 50Hz A.C. Mains.
 - * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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