

Computer Logic Training Board has been designed specifically for the study of Audible Logic Probe using Timer IC. It produces a high frequency signal tone when its tip sense high signal, low frequency tone when its tip sense signal is low and a warbling tone (highlow- high...) when encountering a pulse train. This training board gives a better understanding of the concepts involved in construction of Audible Logic Probe and their properties.

The board is absolutely self contained and requires no other apparatus. Practical experience on this board carries great educative value for Science and Engineering Students.



Object

To study of constructional and operational features of Audible Logic Probe using timer IC. (for TTL and CMOS ICs).

Features

The board consists of the following built-in parts :

01. +5V & +12V D.C. at 100mA, IC regulated power supply internally connected.

02. 0-12V D.C. at 20mA, continuously variable power supply also provided.

03. Digital panel meter (for measurement of DC voltage)

Specifications

Voltage Range : 0-19.99 volt. Resolution : 10mV.

Accuracy : $\pm 0.2\% \pm 1$ digit. I/P Impedance : $\pm 0.0\% \pm 1$ digit.

Display : 3½ digit, 7 segment LED (12.5mm height)

Auto : Polarity indication.

Over Load Indication : Sign of 1 on left and blanking of other digits.

04. Timer IC.

05. Quard Comparator IC.

06. Speaker (8W) for output.

- 07. Selector switch is provided to select the audible Logic Probe for checking the voltage level of TTL and CMOS ICs.
- 08. Adequate no. of other Electronic Components.
- 09. Mains ON/OFF switch and Fuse.
- The unit is operative on 230V $\pm 10\%$ at 50Hz A.C. Mains.
- * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
- * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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