

Computer Logic Training Board has been designed specifically to study the characteristics of CMOS IC. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.



Object

01. To study the operation and characteristics of a CMOS inverter.
 - 1.1 Switching characteristics
 - 1.2 Current sourcing characteristics of a CMOS inverter
 - 1.3 Current sinking characteristics of a CMOS inverter
 - 1.4 Paralleling of outputs (current sourcing)
 - 1.5 Paralleling of outputs (current sinking)
02. To study the operation and characteristics of a 2-input NOR gate.
03. To study the operation and characteristics of a 2-input NAND gate.

Features:

The board consists of the following built in parts :

01. 9V D.C. at 30mA IC regulated Power Supply.
 02. Digital Voltmeter 3½ digit having range 20V D.C
 03. Digital Milliammeter 3½ digit having range 200mA D.C.
 04. Quad, 2-input NOR gate IC.
 05. Quad, 2-input NAND gate IC.
 06. Potentiometer for varying input voltage to CMOS IC
 07. Adequate no. of other electronic components.
 08. Main ON/OFF switch and fuse.
- * Unit is operative on $230 \pm 10\%$ at 50Hz ac mains.
 - * Adequate nos. of patch cords stackable 4mm spring loaded plug, length ½ metre
 - * Good quality, reliable terminals/sockets are provided at appropriate places on panel for connections/observations.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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