

Computer Logic Training Board has been designed specifically for the study of Divide by 1 2-Counter Using IC 74177. Practical experience on this board carries a great educative value for science and Engineering students.



Object:

To Measure Clock Frequency using Divide by 12 Counter.

Features:

The board consists of the following built in parts :

- 01. +5 V DC at 100 mA, IC Regulated Power Supply internally connected.
- 02. Divide by 12 counter IC 74177 and dual 4 input NAND Gate IC 7420.
- 03. Adequate no. of other Electronic Components.
- 04. Mains ON/OFF switch, Fuse & Jewel light.
- * The unit is operative on 230V $\pm 10\%$ at 50Hz AC Mains.
- * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for voltage measurements.
- * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Other Apparatus Required:

- * AF/RF Generator Order Code 16917.
- * Frequency Counter Order Code 16904/16915.

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

Website: www.tesca.in