38699 SOP & POS implementation Trainer is compact, ready to use digital electronics experiment board. It is useful for students to get a practical insight into the implementation of different canonical forms. The various applications of a canonical forms are representing any boolean function as a product of sum or sum of product.
Trainer Has built in clock source, logic high low input facility and LED`s for visual indication of input output states.

OBJECT

01 Design a function sing K-map and verify its performance using SOP and POS FORM.
02 Use QUINE McCLUSKEY method for designing function and realize its Nor Or implementation.

FEATURES

01 Stand alone System
02 Easy illustration of different types of canonical forms.
03 LEDs for visual indication of input and output logic states.
04 SPDT switches for input lgic selection.

TECHNICAL SPECIFICATION

01	DC Power Supply	:	+5 V DC
02	Logic levels		
	+5 V	:	High (logic 1)
	0V	:	Low (logic 0)
03	LED indication	:	LED will be ON for logic high or '1' state and Will be OFF for Logic low or '0' state
04	Weight	:	1 KG (Approx)
05	Dimensions (mm)	:	W340 X H125 X D210

LIST OF ACCESSORIES

01 Patch Cord 2mm length 50 cm Red----------05
02 Patch Cord 2mm length 50cm Black---------05

Note: Specifications are subject to change.

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