38712 The Flip-Flop Demonstrator is an elite training system for the digital laboratories. It is useful in understanding the working of digital flip-flops.

38712 describes S-R, J-K, D and T flipflops. The training system is designed in such a way that for performing any experiment, students have to connect the links by patch cords, so it is easy to be familiar with the inputs and outputs of each flip-flop.

Attractive input and output sections are provided on trainer in such a manner so that multiple experiments can be performed simultaneously.

Features
1. Exclusive and compact design
2. Adaptable illustration of Flip-Flops
3. +5V SMPS Adaptor provided with the trainer for power supply
4. Designed, considering all the safety standards
5. Online product tutorial
6. A low cost training system

Object
1. Study of S-R Flip-Flop and to verify its Transition table
2. Study of J-K Flip-Flop and to verify its Transition table
3. Study of D Flip-Flop and to verify its Transition table
4. Study of T Flip-Flop and to verify its Transition table

Technical Specifications

Input	:	+5V DC
Logic levels		
+5V	:	HIGH(Logic 1)
0V	:	LOW (Logic 0)
Dimensions (mm)	:	W 240 x D 345 x H 110
Weight	:	1kg (approximate)

Note: Specifications are subject to change.

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