

Digital Logic Trainer (CMOS) / Logic Trainer Board based on 74 C/4000 series has been designed to make the students familiar with the study of CMOS ICs and verification of the truth tables of logic gates, flip-flops, Gated & Master Slave JK flip-flops, Schmitt Trigger, Expanders, Binary address, Counters, Shift registers, Multiplexer (Encoder), Demultiplexer (Decoder), 8 Bit D/A Converter and 8 Bit A/D Converter etc. Large area of Bread Board is provided on the front panel for ICs. Students can make easily the circuit on the bread board with the help of other accessories which are provided on the front panel of Digital Logic Trainer. The logic trainer board is quite educative and is a must for Science and Engineering Students.

Specifications:

01. OUTPUT D.C. VOLTAGE : Fixed 5V, Variable 0 to ± 18 V.
02. OUTPUT CURRENT : 1 Amp.
03. LOAD REGULATION : $\pm 1\%$ of the highest specified output voltage.
(NO LOAD TO FULL LOAD)
04. RIPPLE AND NOISE : less than 2 mV.
05. VARIABLE CLOCK FREQUENCY : 1 Hz to 1 MHz by three frequency range & multiplier.
06. LOGIC INPUTS : 16 switches for High/Low
07. OUTPUT INDICATORS : 16, 5 mm bright Red LEDs.
08. SEVEN SEGMENT DISPLAY : 4 digit seven segment display with decoder driver.
09. DIGITAL VOLTMETER : Digital DC voltmeter range 0 - 20V.
10. OPERATING CONDITIONS : 0 to 40°C and 95% R.H. at 40°C.
11. BREAD BOARD : Unique solder - less large size, spring loaded breadboard consisting of 2 Terminal Strips with 640 tie points each and 4 Distribution Strips with 100 tie points each, totalling to 1680 tie points.
12. INPUT VOLTAGE : 230V $\pm 10\%$ at 50 Hz A.C. Mains.
13. ICs PROVIDED : 29 ICs have been provided.



Note : Following ICs or equivalent will be provided.

S.NO.	LOGIC	I.C.	NO. QTY.
01.	QUAD 2-INPUT NAND GATE	74C00	1
02.	QUAD 2-INPUT NOR GATE	74C02	1
03.	INVERTER CIRCUIT	4069	1
04.	QUAD 2-INPUT AND GATE	74C08	1
05.	QUAD 2-INPUT NAND SCHMITT TRIGGER	4093	2
06.	QUAD 2-INPUT OR GATE	74C32	1
07.	DUAL 2-WIDE 2-1/P AOI GATE 4085		2
08.	3-STATE EXPENDABLE 8-FUNCTION 8-INPUT GATE	4048	2
09.	DUAL J-K POSITIVE EDGE TRIGGERED FLIP-FLOP	7470	1
10.	DUAL JK M/S FLIP-FLOP	4027	2
11.	DUAL J-K F-F WITH CLEAR	74C73	2
12.	4-BIT BINARY FULL ADDER	74C83	1
13.	QUAD 2-INPUT EX-OR GATE	4070	1
14.	4-BIT DECADE COUNTER	74C90	3
15.	DIVIDE-BY TWELVE COUNTER	74LS92	1
16.	4-BIT BINARY COUNTER	74C93	1
17.	4-BIT RIGHT-SHIFT/LEFT-SHIFT REGISTER	74C95	1
18.	TRI-STATE QUAD BUFFERS	74C126	1
19.	8-CHANNEL DIGITAL MULTIPLEXER	74LS151	1
20.	4-LINE TO 16-LINE DECODER/ DEMULTIPLEXER	74C154	1
21.	8-BIT D/A CONVERTER	DAC 0808	1
22.	8-BIT A/D CONVERTOR	ADC 0808	1

- * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.
- * Weight : 5 Kg. (Approx).
- * Dimension : W 412 x H 150 x D 310

Note: Specifications are subject to change.

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