## SALIENT FEATURES



- Aesthetically designed injection moulded electronic desk (Master unit) with common experiment resources like Power supplies, Function Generator, switches, indicators, DPM etc. while the slot will carry replaceable expt panels.
- 4 mm sockets arranged on a grid of 19 X 19 mm to receive plug in components useful foe students in non electronic streams of engineering.
- Computer assisted Training through use of Lab viewÒ based executables, optionally supported by variety of virtual instrumentation like toggle switches, leds, DMM, CRO etc. interface through USB IO module.
- Emphasis on troubleshooting skill through fault switches, locate hidden the experiment panels.
- Following Replaceable panels connect to Top Board resources / computer / I/Fon master unit through 64 pin Euro connector.
- Useful for Post Graduate projects and research purpose
- Set of Users Guide provided with each unit.

Technical Specification

| Power Supply | DC Supply | $5 \mathrm{~V} / 1 \mathrm{~A}, 12 \mathrm{~V} / 500 \mathrm{~mA} 0$ to 18 V DC (Variable) / 1A (Isolated) 2nos. With short ckt \& overload protection. Both kept isolated to facilitate either 0 to 36 V or $\pm 18 \mathrm{~V}$ power supply etc. |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | AC Supply | 12-0-12V AC, 150 mA , Short circuit protected. |  |  |
|  | Waveform | Sine, Triangle \& Square wave, output for TTL/CMOS with variable amplitude pot. |  |  |
|  | Frequency | 1 Hz to 1 MHz in 6 ranges, with amplitude \& frequency control pots. |  |  |
|  | Voltage \& Current | 15V p max (Sin/TRG) open ckt. (7.5Vpp into 50 ohm termination) |  |  |
|  | $\begin{aligned} & \text { Optional I/ps } \\ & \text { (On Hind Plate) } \end{aligned}$ | AM | FM | FSK |
|  |  | AM (std.)-I/P voltage $\pm 5 \mathrm{~V}$ ( $100 \%$ modulation) 0V-No Modulation AM (DSBSC) I/Ptg. 09.8 Vpp., O/P Vtg. 02.7 | $\begin{aligned} & 1 / \mathrm{P} \text { voltage } \pm \\ & 400 \mathrm{mV}( \pm \\ & 50 \% \text { modulation }) \end{aligned}$ | I/P upto 500 Hz , $\pm 4.5$ squar wave |
| 3 - phase voltage Generator |  | $6 \mathrm{Vrms} / 50 \mathrm{~Hz} / 120$ degree phase shifted $3 \mathrm{O} / \mathrm{ps}$, max 10 mA loading |  |  |
| Frequency Counter (Optional) |  | 5 digit frequency display, Max 1/P - 10Vpp, 300MV sensitivity, Range : 2MhZ, Max |  |  |
| Logic input switches \& status Indicators |  | Bi-colour buffered LED status indicators 8X2 nos for High/Low indication. Input data Switch (slide switches) X 8nos. |  |  |
| Computer Interface Digital I/O |  | Using switch selectable parallel port or optional USB port (through optional converter supplied ) 8 1/P, 8 O/P 8 Bidirectional I/O Lines (TTL), opto isolated Adaptor to prevent damage to PC |  |  |
|  | Analog I/P | 2nons. of $\pm 9 \mathrm{~V}$ ADC channels, 2nons.of 0-2.5V ADC channels, Max BW - 1 KHz |  |  |
|  | Analog O/P | O/P 0-10V max (optionally 0-2.5V), max load 10 mA |  |  |
| Pulser switches (2nos.) |  | With four debounced outputs (TTL), A, A, B, B |  |  |
| Logic probe |  | detect High/Low, level TTL pulses upto 1 MHz , with bi-colour LEDs to indicate sratus. |  |  |
| 7 segment display |  | 2-digit red led 7 segment with built in BCD to 7 segment decoder (TTL) |  |  |
| Onboard DPM )1no.) |  | Provided with mode/range selection, DC volt-2V.20V \& DC current-2mS/200mA |  |  |
| Mic Pre Amplifier |  | ampilifier function block with DC gain $=1, \mathrm{AC}$ gain $=50$ |  |  |
| L/S Amplifier \& Speaker (optional) |  | Amplifier gain 20, with volume control driving 8ohm, $0.5 \mathrm{~W}-\mathrm{L} / \mathrm{S}$, Mounted on hind plate |  |  |
| Onboard POTS |  | $1 \mathrm{~K}, 1 \mathrm{M}$ (Optional in place of counter module) |  |  |
| Fault Switches |  | 14 nos. of gnd referred fault switches hidden under replaceable expt. Panel on pcb carrying 64 Pin Euro connector |  |  |
| Accessories |  | 1) Parallel port 25 pin cable <br> 2) Virtual Lab CD <br> 3) USB 1/O module (optional) with type A to mini B cable <br> 4) dynamic Mic or electret Mic with builtin bias (cell) (optional) |  |  |
| Operating Voltage |  | 220/240Vac Switch settable $\pm 10 \% 50 \mathrm{~Hz} / 80 \mathrm{VA}$ |  |  |
| Mechinical Dimensions |  | A) Master Unit: $460 \mathrm{~mm}(\mathrm{~W}), 160 \mathrm{~mm}(\mathrm{H}), 350 \mathrm{~mm}(\mathrm{D})$, Net weight : 7.5 Kg . Gross Wt: 9.5 Kg . B) PCB Panel : 439mm X 209mm |  |  |

Modular expt. panels offered (At least select one or more)

1) Digital Logic panel/TCAT1
[Provided with 227 banana sockets]

- Consist of DIP sockets : 14 pin (7 nos), 16 pin (5 nos), 24nos. of buffered leds using 3nos. LS245 as LED driver, 4 TTL clock outputs $1,10,100 \mathrm{~Hz}, 1 \mathrm{KHz}$.
- Generic ICs used : 7400 (NAND), 7408 (AND), 7432 (OP), 7495 (SHIFT REG), 7404 (NOT), 7486 (EX-OR), 7476 X 2Nos. (J-KFF), 7490 (DECADE COUNTER), 74138 (DECODER), 74148 (ENCODER), CD4051 (MUX/DEMUX) ,1X3 extender (2nos).
- Experiment covered

Note: Specifications are subject to change.

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Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com
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iii) Combinational circuit adder, subtractor, code converter Like Gray code, 7 seg BCD, Hex Excess-3, Parity checker, Encodr/ Decoder, Multiplexer / Demultiplexer
iV) Sequential Logic like Filp-Flpos- R-S, J-K, T,D. Counters- Async/Sync, decade, ring/twisted, Divide by N (modulo N)
V) Multivibrator circuit (Mono/Bistable/Astable).
Vi) Applications- Traffic signal control, Staircase Lampetc.
2) Plug-in components panel / TCAT2
[Provided with 205 banana sockets]

- Consists of 19 grid of 4 mm sockets, All plug in discrete components are housed in Acrylic transparent top module of size 50.5 mm X 32.5 mm with 4 (8optional) plugs to facilitate easy viewing \& handing w/o fear of damage. Useful for students w/o electronic background.
- Plug in components (TTM) diode (4nos), resistor (8nos), Potentiometers (2nos), led (2nos), transistor (4nos), Relay (1no) etc. Qty = 25nos.assorted


## 3) Bread board panel / TCAT3

[Connectivity through 64 X 2 tie points]

- Consists of 3360 tie points bread board and 64 X 2 connectivity tie points offering top board resources like Function Generator, statue switches, logics indicators, power supply, $8 ; / \mathrm{O}$ lines, pulser outputs, 4 TTL clock output $1,10,100 \mathrm{~Hz}$. 1 khz etc. for easy connectivity using 22/24 SWG single wires, 28 pin ZIF Socket, Input data switches - 8 Nos, pulser Switch-1No., RC Circuits-4nos., On board por 100-K - 1 No., Bicolour buffered LED status indicators 8 X 2 for high low indication BS5 to Bread Board converters - 8Nos., BNC to banana converter- 2 Nos .


## 4) Digital panel II / TCAT4

[Provided with 269 banana sockets]

- Consists of DIP sockets : 14 pin ( 7 nos), 16 pin ( 5 nos), 28 pin ZIF sockets ( 1 no.) 4 TTL clock outputs-1, $10,100 \mathrm{~Hz}, 1 \mathrm{KHZ}$ Bi-colour buffered LED status indication 8X2 for high/low indication, Input data Switches 8 nos., NO_NC pulser switch -1no./ On board pot 100K-1no., RC ckts for Mono stable M.V. RC ckts for ADV.BNC to banana convertor-2nos.
- Generic ICs Used : TTL, COMS ICs like 74280 (Parity Generator), 7407 (buffer), 74 CHT 14 (Schmitt Inverter), CD4011(NAND), 7485 (Comparator), 74191 (Counter), 74123 (Multi-vibrator),CD4013 (D/F/F), CD4052(MUX/DEMUX),CD4001(NOR), Cd4093 (Schmitt NAND), CD4007 (CMOS Inverter).
- Experiments Covered : Study of TTL, CMOS characteristics, schmitt gate circuit, Circuite using NAND gate, Multiplexer circuits, Opencollector gate circuits, Parity generating circuits High speed monostable circuits, Comparator circuits, Counter circuits, CMOS device characteristics, 12bit ADC DAC optional using ZIF sockets.


## 5) Analog computer/TCAT5

[Provided with 128 banana sockets]

- Function blocks: Analog multipliers (3nos), op-amp inverting (2nos), Op-amp Basic (2nos), op-amp full (2nos), bread board for general purpose circuits Digital to analog converters (2nos.), diodes, transistors trimmers, voltage regulators
- Experiments covered: Study the characteristics of negative feedback amplifiers and design of n instrumentation amplifier, Study the characteristics of regenerative feedback, Study the characteristics of integrators and differentiator circuit, Design of Analog Filters Design of a self-tuned fi'ter, Design unction generator and convert it to voltage-Controlled Oscillator / FM generator Design of a phase Lock loop (PLL), automatic Gain control (AGC) automatic volume Control (AVC), DC_DC converter, Design low dropout (LDO) regulator, To study the parameters of LDO integrated circuit, To study the parameters of DC-DC convertor sing Stage Amplifier, Design of a Digitally programmable square and Triangular wave generator/oscillator.

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