



SALIENT FEATURES

- Aesthetically designed injection moulded electronic desk (Master unit) with common experiment resources like Power supplies, Function Generator, switches, indicators, DPM etc. while the slot will carry replaceable expt panels.
- 4 mm sockets arranged on a grid of 19 X 19mm to receive plug in components useful foe students in non electronic streams of engineering.
- Computer assisted Training through use of Lab viewÒ based executables, optionally supported by variety of virtual instrumentation like toggle switches, leds, DMM, CRO etc. interface through USB IO module.
- Emphasis on troubleshooting skill through fault switches, locate hidden the experiment panels.
- Following Replaceable panels connect to Top Board resources / computer / I/Fon master unit through 64 pin Euro connector.
- Useful for Post Graduate projects and research purpose
- Set of Users Guide provided with each unit.

Technical Specification

Power Supply	DC Supply	5V / 1A, 12V / 500mA 0 to 18V DC (Variable) / 1A (Isolated) 2nos. With short ckt & overload		
		protection. Both kept isolated to facilitate either 0 to $36V$ or $\pm 18V$ power supply etc.		
	AC Supply	12-0-12V AC, 150mA, Short circuit protected.		
Function Generator	Waveform	Sine, Triangle & Square wave, output for TTL/CMOS with variable amplitude pot.		
	Frequency	1 Hz to 1MHz in 6 ranges, with amplitude & frequency control pots.		
	Voltage & Current			
	Optional I/ps	AM	FM	FSK
ion	(On Hind Plate)	AM (std.)-I/P voltage ± 5V (100% modulation) 0V-No	l/P voltage ±	I/P upto 500Hz,
JCt		Modulation AM (DSBSC) I/Ptg. 09.8 Vpp., O/P Vtg. 0-	400mV (±	± 4.5 squar wave
Fui		2.7	50% modulation)	
3 - phase voltage Generator		6Vrms/50Hz/120 degree phase shifted 3 O/ps, max 10mA loading		
Frequency Counter (Optional)		5 digit frequency display, Max I/P - 10Vpp, 300MV sensitivity, Range : 2MhZ, Max		
Logic input switches &		Bi-colour buffered LED status indicators 8X2 nos for High/Low indication.		
status Indicators		Input data Switch (slide switches) X 8nos.		
Computer Interface Digital I/O Analog I/P Analog O/P		Using switch selectable parallel port or optional USB port (through optional converter supplied) 8		
		1/P, 8 O/P 8 Bidirectional I/O Lines (TTL), opto isolated Adaptor to prevent damage to PC		
		2nons. of \pm 9V ADC channels, 2nons.of 0-2.5V ADC channels, Max BW - 1KHz		
		O/P 0-10V max (optionally 0-2.5V), max load 10 mA		
Pulser switches (2nos.)		With four debounced outputs (TTL), A, A,B,B		
Logic probe		detect High/Low, level TTL pulses upto 1 MHz, with bi-colour LEDs to indicate status.		
7 segment display		2-digit red led 7 segment with built in BCD to 7 segment decoder (TTL)		
Onboard DPM)1no.)		Provided with mode/range selection, DC volt-2V.20V & DC current-2mS/200mA		
Mic Pre Amplifier		ampilifier function block with DC gain = 1, AC gain = 50		
L/S Amplifier &	Speaker	Amplifier gain 20, with volume control driving 80hm, 0.5	5W- L/S, Mounted o	n hind plate
(optional)				
Onboard POTS		1K, 1M (Optional in place of counter module)		
Fault Switches		14 nos. of gnd referred fault switches hidden under replaceable expt. Panel on pcb carrying 64 Pin		
		Euro connector		
Accessories		1) Parallel port 25 pin cable		
		2) Virtual Lab CD		
		3) USB I/O module (optional) with type A to mini B cable		
		4) dynamic Mic or electret Mic with builtin bias (cell) (optional)		
Operating Voltage		220/240Vac Switch settable ± 10% 50Hz/80 VA		
Mechinical Dime	ensions	A) Master Unit: 460mm (W), 160mm (H), 350mm (D), Net weight : 7.5Kg. Gross Wt: 9.5Kg. B)		
		PCB Panel : 439mm X 209mm		

Modular expt. panels offered (At least select one or more)

1) Digital Logic panel/TCAT1

- [Provided with 227 banana sockets]
- Consist of DIP sockets : 14 pin (7 nos), 16 pin (5 nos), 24nos. of buffered leds using 3nos. LS245 as LED driver, 4 TTL clock outputs 1, 10, 100Hz, 1KHz.
- Generic ICs used : 7400 (NAND), 7408 (AND), 7432 (OP), 7495 (SHIFT REG), 7404 (NOT), 7486 (EX-OR), 7476 X 2Nos. (J-KFF), 7490 (DECADE COUNTER), 74138 (DECODER),74148 (ENCODER), CD4051 (MUX/DEMUX) ,1X3 extender (2nos).
- Experiment covered
 - i) Basic logic gates experiments-OR, AND INVERTER, NOR NAND, EX-OR, EX-NOR
 II) Boolean Algebra theorems-25nos., Kaurnaugh Map
- Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension, Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India, Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com Website: www.tesca.in





- iii) Combinational circuit adder, subtractor, code converter Like Gray code, 7 seg BCD, Hex Excess-3, Parity checker, Encodr/ Decoder, Multiplexer / Demultiplexer
- iV) Sequential Logic like Filp-Flpos- R-S, J-K, T,D. Counters- Async/Sync, decade, ring/twisted, Divide by N (modulo N)
- V) Multivibrator circuit (Mono/Bistable/Astable).
- Vi) Applications- Traffic signal control, Staircase Lampetc.

2) Plug-in components panel / TCAT2

[Provided with 205 banana sockets]

- Consists of 19 grid of 4 mm sockets, All plug in discrete components are housed in Acrylic transparent top module of size 50.5mm X 32.5mm with 4 (8optional) plugs to facilitate easy viewing & handing w/o fear of damage. Useful for students w/o electronic background.
- Plug in components (TTM) diode (4nos), resistor (8nos), Potentiometers (2nos), led (2nos), transistor (4nos), Relay (1no) etc. Qty = 25nos.assorted

3) Bread board panel / TCAT3

[Connectivity through 64 X 2 tie points]

• Consists of 3360 tie points bread board and 64 X 2 connectivity tie points offering top board resources like Function Generator, statue switches, logics indicators, power supply, 8:/O lines, pulser outputs, 4 TTL clock output 1,10,100Hz. 1khz etc. for easy connectivity using 22/24 SWG single wires, 28 pin ZIF Socket, Input data switches - 8 Nos, pulser Switch-1No., RC Circuits-4nos., On board por 100-K -1No., Bicolour buffered LED status indicators 8 X 2 for high low indication BS5 to Bread Board converters - 8Nos., BNC to banana converter- 2Nos.

4) Digital panel ll / TCAT4

[Provided with 269 banana sockets]

- Consists of DIP sockets: 14 pin (7nos), 16 pin (5nos),28pin ZIF sockets (1no.) 4TTL clock outputs-1, 10, 100Hz, 1KHZ Bi-colour buffered LED status indication 8X2 for high/low indication, Input data Switches 8 nos., NO_NC pulser switch -1no./ On board pot 100K-1no., RC ckts for Mono stable M.V. RC ckts for ADV.BNC to banana convertor-2nos.
- Generic ICs Used : TTL, COMS ICs like 74280 (Parity Generator), 7407 (buffer), 74CHT14 (Schmitt Inverter), CD4011(NAND), 7485 (Comparator), 74191 (Counter), 74123 (Multi-vibrator), CD4013 (D/F/F), CD4052(MUX/DEMUX), CD4001(NOR), Cd4093 (Schmitt NAND), CD4007 (CMOS Inverter).
- Experiments Covered : Study of TTL, CMOS characteristics, schmitt gate circuit , Circuite using NAND gate, Multiplexer circuits, Opencollector gate circuits, Parity generating circuits High speed monostable circuits, Comparator circuits, Counter circuits, CMOS device characteristics, 12bit ADC DAC optional using ZIF sockets.

5) Analog computer /TCAT5

[Provided with 128 banana sockets]

- Function blocks: Analog multipliers (3nos), op-amp inverting (2nos), Op-amp Basic (2nos), op-amp full (2nos), bread board for general purpose circuits Digital to analog converters (2nos.), diodes, transistors trimmers, voltage regulators
- Experiments covered: Study the characteristics of negative feedback amplifiers and design of n instrumentation amplifier, Study the characteristics of regenerative feedback, Study the characteristics of integrators and differentiator circuit, Design of Analog Filters Design of a self-tuned filter, Design unction generator and convert it to voltage-Controlled Oscillator / FM generator Design of a phase Lock loop (PLL), automatic Gain control (AGC) automatic volume Control (AVC), DC_DC converter, Design low dropout (LDO) regulator, To study the parameters of LDO integrated circuit, To study the parameters of DC-DC convertor sing Stage Amplifier, Design of a Digitally programmable square and Triangular wave generator/oscillator.

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