

Digital Logic Trainer (TTL) / Logic Trainer Board based on 74 series has been designed specifically to make the students familiar with the study of TTL ICs and verification of the truth table of logic gates, flip-flops, Gated & Master Slave JK flip-flops, Schmitt Trigger, Expanders, Binary address, Counters, Shift registers, Multiplexer (Encoder), Demultiplexer (Decoder), 8 Bit D/A Converter and 8 Bit A/D Converter etc. Large area of Bread Board is provided on the front panel for ICs. Students can make the circuit easily on the Bread Board with the help of other accessories which are provided on the front panel of Digital Logic Trainer.

Practical experience on this trainer/board carries great educative value for Science and Engineering Students.

Specifications:

01. OUTPUT D.C. VOLTAGE : Fixed 5V and 0 - $\pm 18V$.
02. OUTPUT CURRENT : 1 Amp.
03. LOAD REGULATION : $\pm 1\%$ of the highest specified output voltage. (NO LOAD TO FULL LOAD)
04. RIPPLE AND NOISE : less than 2 mV.
05. VARIABLE CLOCK FREQUENCY : 1 Hz to 1 MHz by three frequency range & multiplier.
06. LOGIC INPUTS : 16 switches for High/Low
07. OUTPUT INDICATORS : 16, 5 mm bright Red LEDs.
08. SEVEN SEGMENT DISPLAY : 4 digit seven segment display with decoder driver.
09. DIGITAL VOLTMETER : Digital DC voltmeter range 0 - 20V.
10. OPERATING CONDITIONS : 0 to 40°C and 95% R.H. at 40°C.
11. BREAD BOARD : Unique solder - less large size, spring loaded breadboard consisting of 2 Terminal Strips with 640 tie points each and 4 Distribution Strips with 100 tie points each, totalling to 1680 tie points.
12. INPUT VOLTAGE : 230V $\pm 10\%$ at 50 Hz A.C. Mains.
13. ICs PROVIDED : 29 ICs have been provided.

Note : Following ICs or equivalent can be provided.



S.NO.	LOGIC IC.	NO. QTY.
01.	QUAD 2-INPUT NAND GATE 7400	1
02.	QUAD 2-INPUT NOR GATE 7402	1
03.	HEX INVERTER 7404	1
04.	QUAD 2-INPUT AND GATE 7408	1
05.	DUAL 4-INPUT NAND SCHMITT TRIGGER 7413	2
06.	QUAD 2-INPUT OR GATE 7432	1
07.	EXPANDABLE DUAL 2-WIDE 2-INPUT AOI GATE 7450	2
08.	DUAL 4-INPUT EXPANDER 7460	2
09.	EDGE - TRIGGERED FLIP-FLOP 7470	1
10.	DUAL JK M/S FLIP-FLOP 4027	2
11.	DUAL JK-FLIP-FLOP 7473	2
12.	4 BIT FULL ADDER 7483	1
13.	QUAD 2-INPUT EXCLUSIVE OR-GATE 7486	1
14.	DECADE COUNTER 7490	3
15.	DIVIDE-BY-TWELVE COUNTER 7492	1
16.	4-BIT BINARY RIPPLE COUNTER 7493	1
17.	4-BIT SHIFT REGISTER 7495	1
18.	QUAD 3-STATE BUFFER 74126	1
19.	8-INPUT MULTIPLEXER 74151	1
20.	1-OF-16 DECODER/DEMULTIPLEXER 74154	1
21.	8-BIT D/A CONVERTER DAC 0808	1
22.	8-BIT A/D CONVERTER ADC 0808	1

* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension,
Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India,
Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in