



Specifications:-

- P0082 (Terasic) is a DE0-Nano Development board is a compact-sized FPGA development platform suited for prototyping circuit designs such as robots and "portable" projects. The board is designed to be used in the simplest possible implementation targeting the Cyclone IV device up to 22,320 logic elements (LEs). This platform: Allows user to extend designs beyond the DE0-Nano board with two external general-purpose I/O (GPIO) headers, Allows user to handle larger data storage and frame buffering with on-board memory devices including SDRAM and EEPROM, Provides enhanced user peripheral with LEDs and push buttons, Is small and lightweight, Is reconfigurable without requiring superfluous hardware, Is suitable for mobile designs where portable power is crucial, as it provides three power scheme options including a USB Mini-AB port, 2 pin external power header and two DC 5-V pins.
- Cyclone IV EP4CE22F17C6N FPGA
- Configuration & set-up elements: On-Board USB Blaster ckt, serial configuration device EPCS16
- Memory devices: 32MB SDRAM, 2Kb I2C EEPROM
- G-sensor includes ADI ADXL345, 3-axis accelerometer with high resolution (13bits)
- 8-channel, 12bit Analogue to digital converter
- On-Board 50MHz clock oscillator
- Altera complete design suite
- Features DE0-Nano quick start guide
- **USB Mini-B cable**

Note: Specifications are subject to change, Photos shown above are Indicative, Actual Product can Vary.



Export Sales: +91-9829132777
India Sales: +91-9588842361



IT-2013, Ramchandrapura Industrial Area,
Sitapura Extension, Jaipur-302022, India.



info@tesca.in
www.tescaglobal.com