TECHNICAL SPECIFICATIONS:-

- 77K Programmable Logic Elements
- 4884 Kbits embedded memory
- Six Fractional PLLs
- Two Hard Memory Controllers
- Six 3.125G Transceivers
- Configuration and Debug: Quad Serial Configuration device – EPCQ256 on FPGA
- On-Board USB Blaster (Normal Type-B USB connector)
- JTAG and AS mode configuration supported
- Memory Device: 4Gb LPDDR2 x32 bits data bus

Note: Specifications are subject to change, Photos shown above are Indicative, Actual Product can Vary.